	Type	L #	Hits	Search Text	DBs
1	BRS	L1	0	((delay adj lock adj loop DLL) and divider with delay and phase adj (detector comparator) and (multiplexer multiplexor mux multiplexing) with divider with reference with phase adj (detector comparator)).clm.	US-PGPUB
5	BRS	L5	0	((dividing divider divided division) and (delaying delay) and phase adj (detector comparator) and (selector selecting multiplexor multiplexing multiplexer mux) with reference with divided adj clock with phase adj (detector comparator) and (dividing divider divided division) near3 reference).clm.	US-PGPUB

	Type	L #	Hits	Search Text	DBs
8	BRS	L8	1	((dividing divider divided division) with (delaying delay) with reference and phase adj (detector comparator) with delay with (dividing divider divided division) and controller with (dividing divider divided division) with phase adj (detector comparator) with delay$3 with phase adj (error change difference)).clm.	US-PGPUB
9	BRS	L9	1	((dividing divider divided division) with (delaying delay) with reference and phase adj (detector comparator) with delay with (dividing divider divided division) and (controller programm$3) with (dividing divider divided division) with (error change difference)).clm.	US-PGPUB